VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 500 090, Telangana, India

TIMETABLE FOR II B.TECH, I SEMESTER (R19), SUPPLEMENTARY EXAMINATION – NOVEMBER/DECEMBER, 2026

Branch	20th November 2026 10:00 A.M. to 1:00 P.M.	23rd November 2026 10:00 A.M. to 1:00 P.M.	25th November 2026 10:00 A.M. to 1:00 P.M.	27th November 2026 10:00 A.M. to 1:00 P.M.	1st December 2026 10:00 A.M. to 1:00 P.M.
	Friday	Monday	Wednesday	Friday	Tuesday
CE	Probability, Statistics and Time Series (19BS1MT12)	Strength of Materials – I (19PC1CE01)	Fluid Mechanics (19PC1CE02)	Building Materials, Construction and Planning (19PC1CE03)	Surveying and Geomatics (19PC1CE04)
EEE	Complex Analysis and Transforms (19BS1MT09)	Electromagnetic Fields (19PC1EE01)	Electrical Machines – I (19PC1EE02)	Electronic Devices and Circuits (19PC1EC02) **[Common to EEE, ECE]**	Digital System Design (19PC1EC03) **[Common to EEE, ECE]**
ME	Partial Differential Equations and Numerical Methods (19BS1MT10) **[Common to ME, AE]**	Metallurgy and Materials Engineering (19PC1ME01) **[Common to ME, AE]**	Manufacturing Processes (19PC1ME02)	Fluid Mechanics and Machinery (19PC1ME03)	Thermodynamics (19PC1ME04) **[Common to ME, AE]**
ECE	Complex Analysis & Special Functions (19BS1MT08) **[Common to ECE, EIE]**	Probability Theory & Stochastic Processes (19PC1EC01)	Signals and Systems (19PC1EC04) **[Common to ECE, EIE]**	Electronic Devices and Circuits (19PC1EC02) **[Common to EEE, ECE]**	Digital System Design (19PC1EC03) **[Common to EEE, ECE]**
CSE	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Object Oriented Programming through C++ (19PC1IT01) **[Common to CSE, DS, CyS, IT]**	Design and Analysis of Algorithms (19PC1CS03) **[Common to CSE, DS, IOT, CyS, IT]**	Database Management systems (19PC1CS04) **[Common to CSE, DS, IOT, CyS, IT]**
EIE	Complex Analysis & Special Functions (19BS1MT08) **[Common to ECE, EIE]**	Electronics Circuits – I (19PC1EI01)	Signals and Systems (19PC1EC04) **[Common to ECE, EIE]**	Sensors & Signal Conditioning (19PC1EI02)	Electronic Measurements (19PC1EI03)
IT	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Object Oriented Programming through C++ (19PC1IT01) **[Common to CSE, DS, CyS, IT]**	Design and Analysis of Algorithms (19PC1CS03) **[Common to CSE, DS, IOT, CyS, IT]**	Database Management systems (19PC1CS04) **[Common to CSE, DS, IOT, CyS, IT]**

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AE	Partial Differential Equations and Numerical Methods (19BS1MT10) **[Common to ME, AE]**	Metallurgy and Materials Engineering (19PC1ME01) **[Common to ME, AE]**	Mechanics of Solids (19PC1ME08)	Automotive Chassis (19PC1AE01)	Thermodynamics (19PC1ME04) **[Common to ME, AE]**
CSBS	Formal Language and Automata Theory (19PC1CB01)	Computer Organization and Architecture (19PC1CB02)	Object Oriented Programming (19PC1CB03)	Computational Statistics (19PC1CB04)	Software Engineering (19PC1CB05)
CS-CyS	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Object Oriented Programming through C++ (19PC1IT01) **[Common to CSE, DS, CyS, IT]**	Design and Analysis of Algorithms (19PC1CS03) **[Common to CSE, DS, IOT, CyS, IT]**	Database Management systems (19PC1CS04) **[Common to CSE, DS, IOT, CyS, IT]**
CS-AIML	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Software Engineering (19PC1CS06) **[Common to AIML, IOT]**	Java Programming (19PC1IT02)	Computer Organization (19PC1IT03)
CS-DS	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Object Oriented Programming through C++ (19PC1IT01) **[Common to CSE, DS, CyS, IT]**	Design and Analysis of Algorithms (19PC1CS03) **[Common to CSE, DS, IOT, CyS, IT]**	Database Management systems (19PC1CS04) **[Common to CSE, DS, IOT, CyS, IT]**
CS-IOT	Digital Logic Design (19PC1CS01) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Mathematical Foundations for Computer Science (19PC1CS02) **[Common to CSE, AIML, DS, IOT, CyS, IT]**	Software Engineering (19PC1CS06) **[Common to AIML, IOT]**	Design and Analysis of Algorithms (19PC1CS03) **[Common to CSE, DS, IOT, CyS, IT]**	Database Management systems (19PC1CS04) **[Common to CSE, DS, IOT, CyS, IT]**

Controller of Examinations

CONTROLLER OF EXAMINATIONS
VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY
(Autonomous under JNTUH, Hyderabad)
Pragathi Nagar, Nizampet (S.O.),
Hyderabad, Telangana - 500 090. INDIA

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Note : 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY.
2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.